

# Notice of Allowability

Application No.

10/086,214

Examiner

JAMES C. KERVEROS

Applicant(s)

WANG ET AL.

Art Unit

2138

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to Amended Brief on Appeal filed August 31, 2006.
2. ☒ The allowed claim(s) is/are 85-103. Renumbered as Claims 1-19.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All. b) ☐ Some\* c) ☐ None of the:
    1. ☐ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

## Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO/SB/08),  
Paper No./Mail Date 1/27/05
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material

5. ☐ Notice of Informal Patent Application
6. ☐ Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

JAMES C. KERVEROS  
Examiner  
Art Unit 2138

### **NOTICE OF ALLOWANCE**

This is a Notice of Allowance in response to Amended Brief on Appeal filed August 31, 2006. Claims 1-84 have been cancelled.

Claims 85-103 were previously on Appeal from the final rejection of the prior Office Action mailed October 5, 2006.

Applicant's request for reconsideration, filed with Appeal Brief on August 31, 2006 with respect to claims 85-103, of the prior Office action is persuasive and, therefore, the finality of that action is withdrawn.

### ***Allowable Subject Matter***

Claims 85-103 are allowed. Renumbered as Claims 1-19.

### **REASONS FOR ALLOWANCE**

The following is an examiner's statement of reasons for allowance:

The prior arts of record taken alone or in combination fail to teach, anticipate, suggest or render obvious a DFD (design-for-debug) circuitry in an integrated circuit for debugging selected fault types in scan cores, including among other limitations, "a DFD selector for indicating which scan cores and selected fault types will be debugged or diagnosed simultaneously, and a multiplexer for connecting the DFD selector and the scan connector to a TAP (test access port) controller in said integrated circuit", recited in the independent claim 85.

Independent claim 100 recites a method for debugging selected fault types in scan cores using an embedded DFD (design-for-debug) circuitry in an integrated circuit, including among other limitations, the steps of "issuing a SELECT command for shifting in selected scan cores and the selected fault types to be debugged or diagnosed to the DFD selector of the DFD circuitry in the scan cores; issuing a first SHIFT command or a first plurality of SHIFT\_CHAIN commands for shifting in a predetermined scan pattern to all scan cells within selected scan chains in the scan cores for diagnosis; and issuing a second SHIFT command or a second plurality of SHIFT\_CHAIN commands for shifting a new predetermined scan pattern into and output response out of all the scan cells within the selected scan chains in the scan cores for diagnosis".

Claims 86-99 and 101-103 are directly or indirectly depended upon independent claims 85 and 100 and therefore are also allowable. Consequently, independent claims 85-103 are allowed over the prior arts of record.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to JAMES C. KERVEROS whose telephone number is (571) 272-3824. The examiner can normally be reached on 9:00 AM TO 5:00 PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert Decady can be reached on (571) 272-3819. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Date: 19 October 2006  
Office Action: Allowance

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